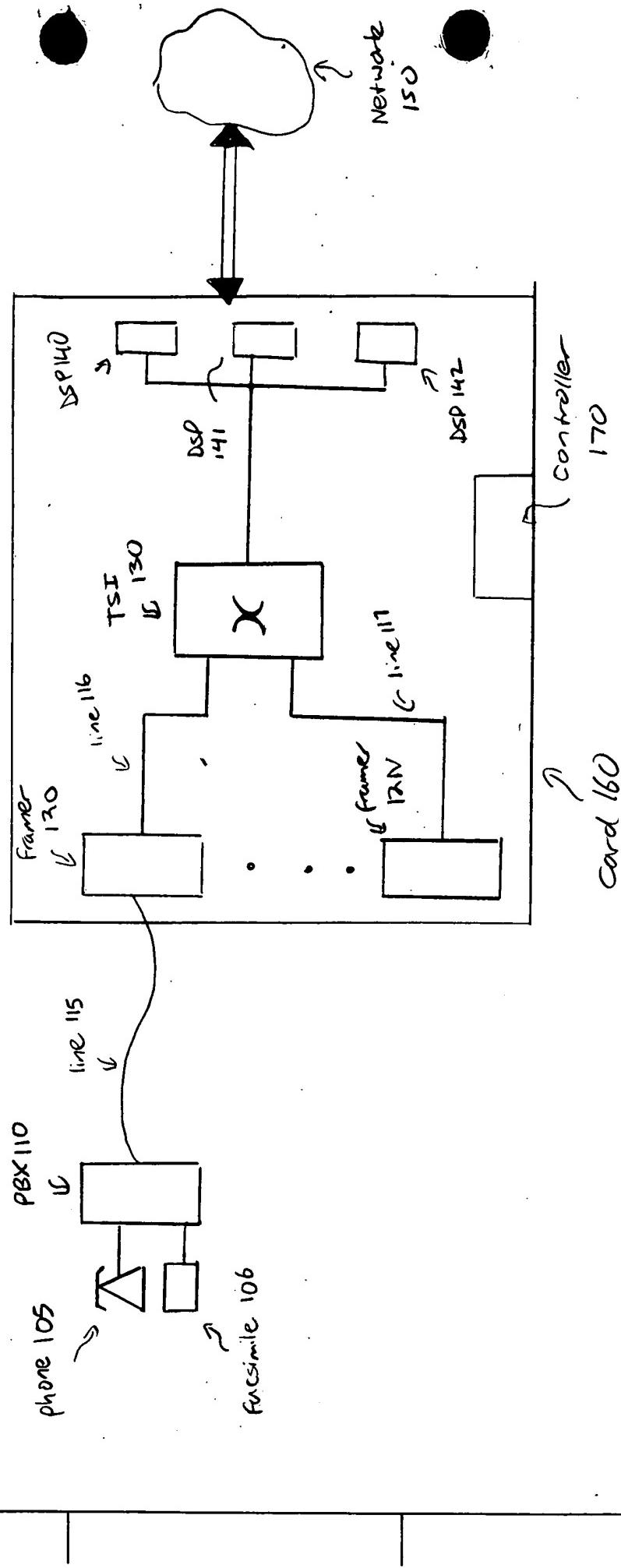


Figure 1 (PRIOR ART)



000000000000000000000000

5

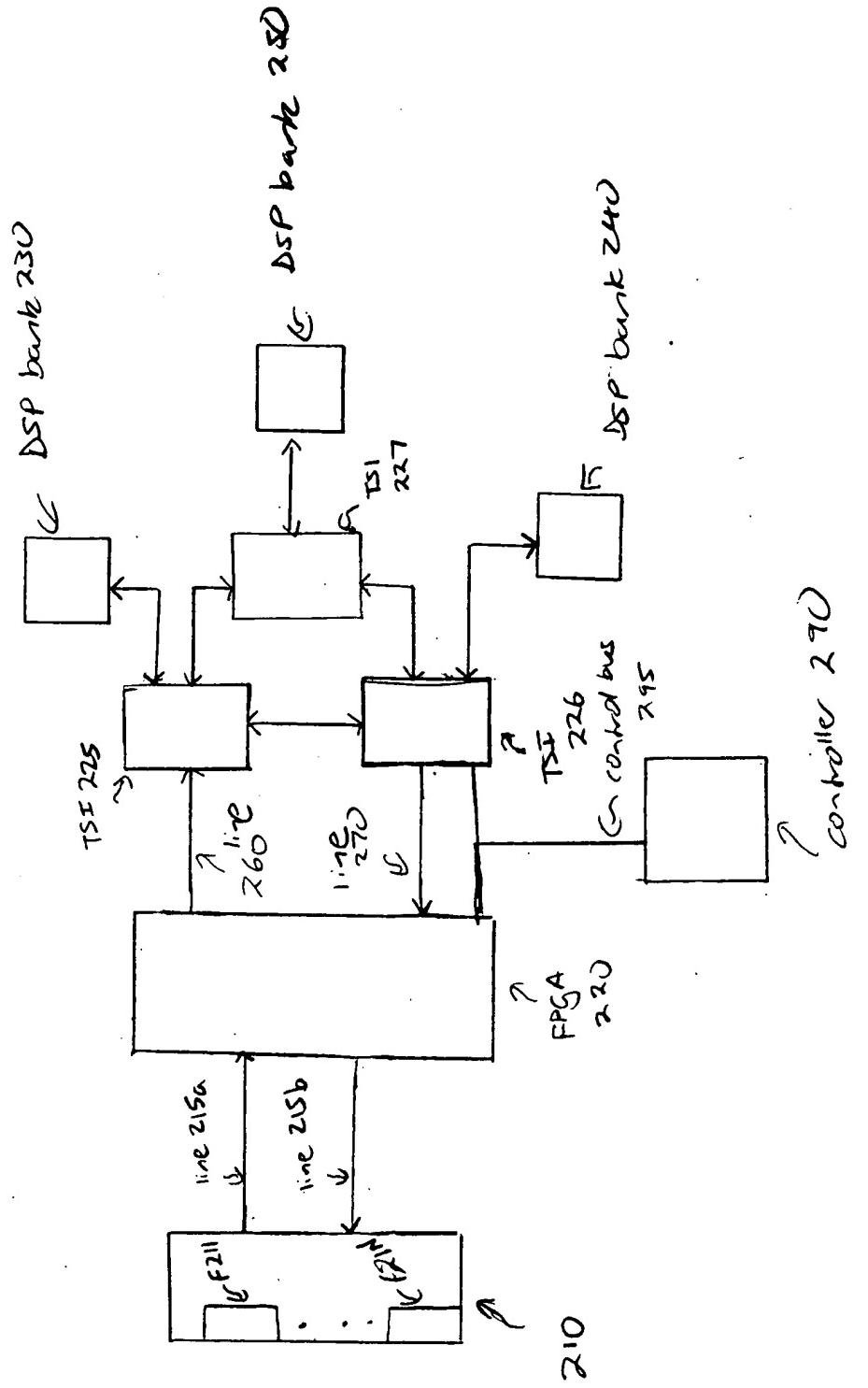
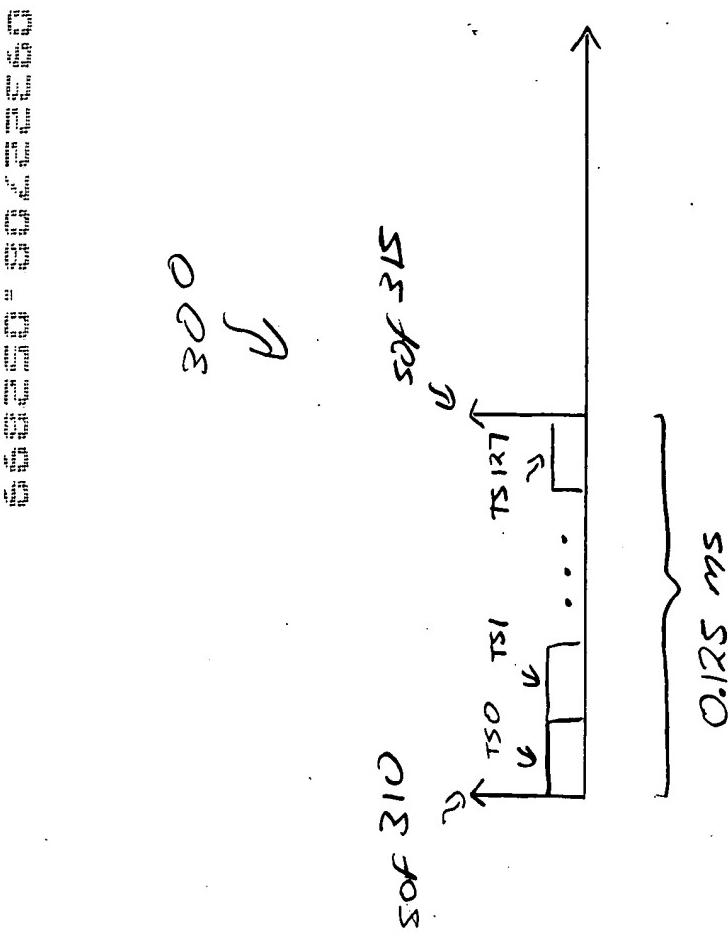


Figure 2

ANIVALE  
22-141 50 SHEETS  
22-142 100 SHEETS  
22-144 200 SHEETS

Figure 3



AMICO  
22-141 50 SHEETS  
22-142 100 SHEETS  
22-144 200 SHEETS

FPGA 400

V

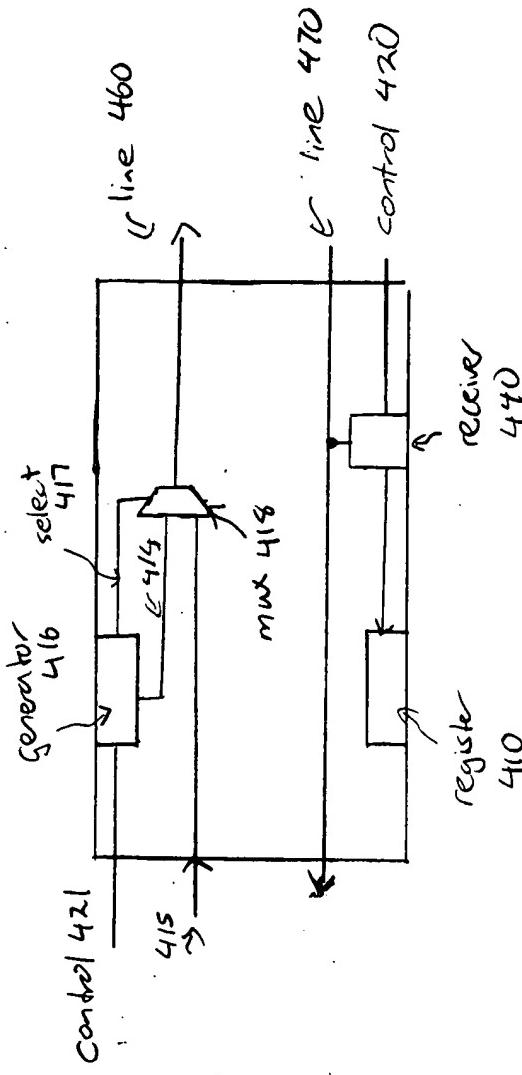
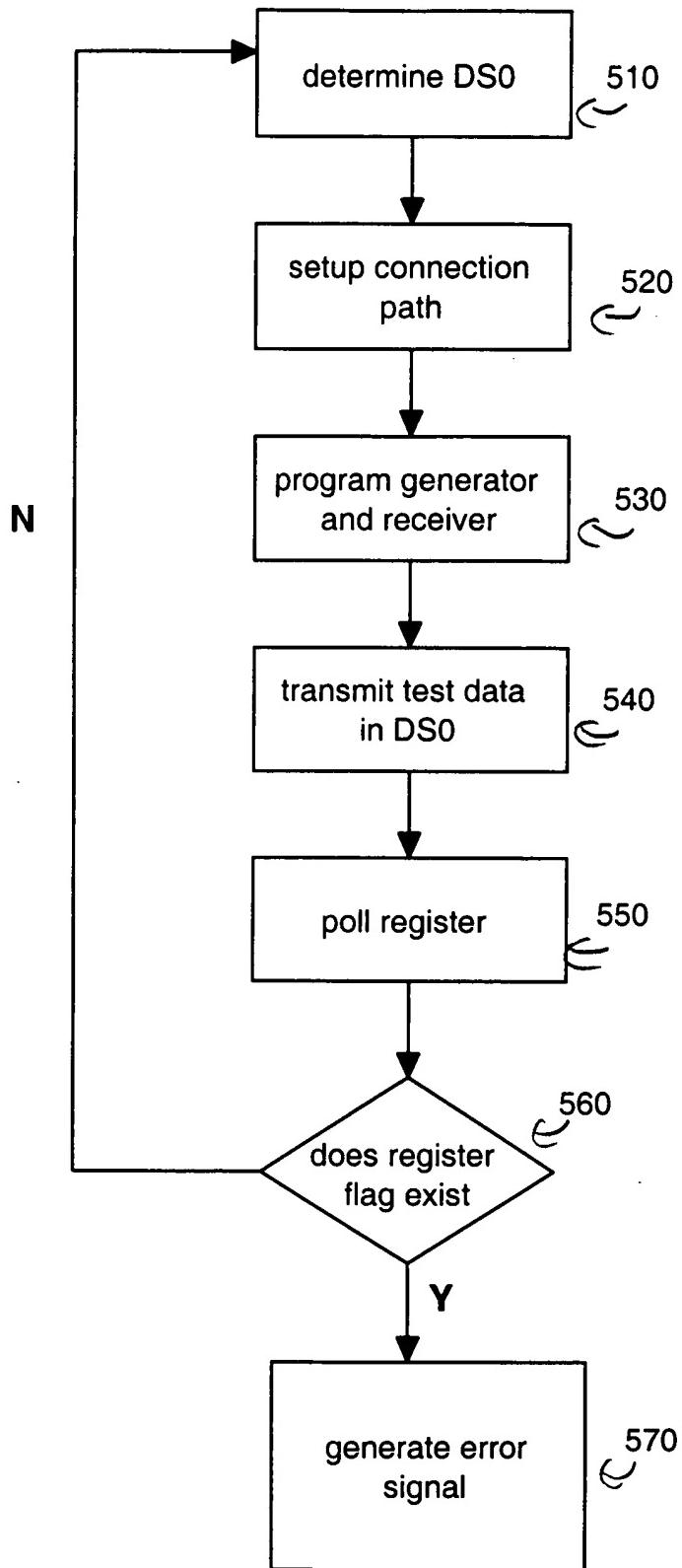


Figure 4

22-141 50 SHEETS  
22-142 100 SHEETS  
22-144 200 SHEETS  
AMPAUD



**Figure 5**

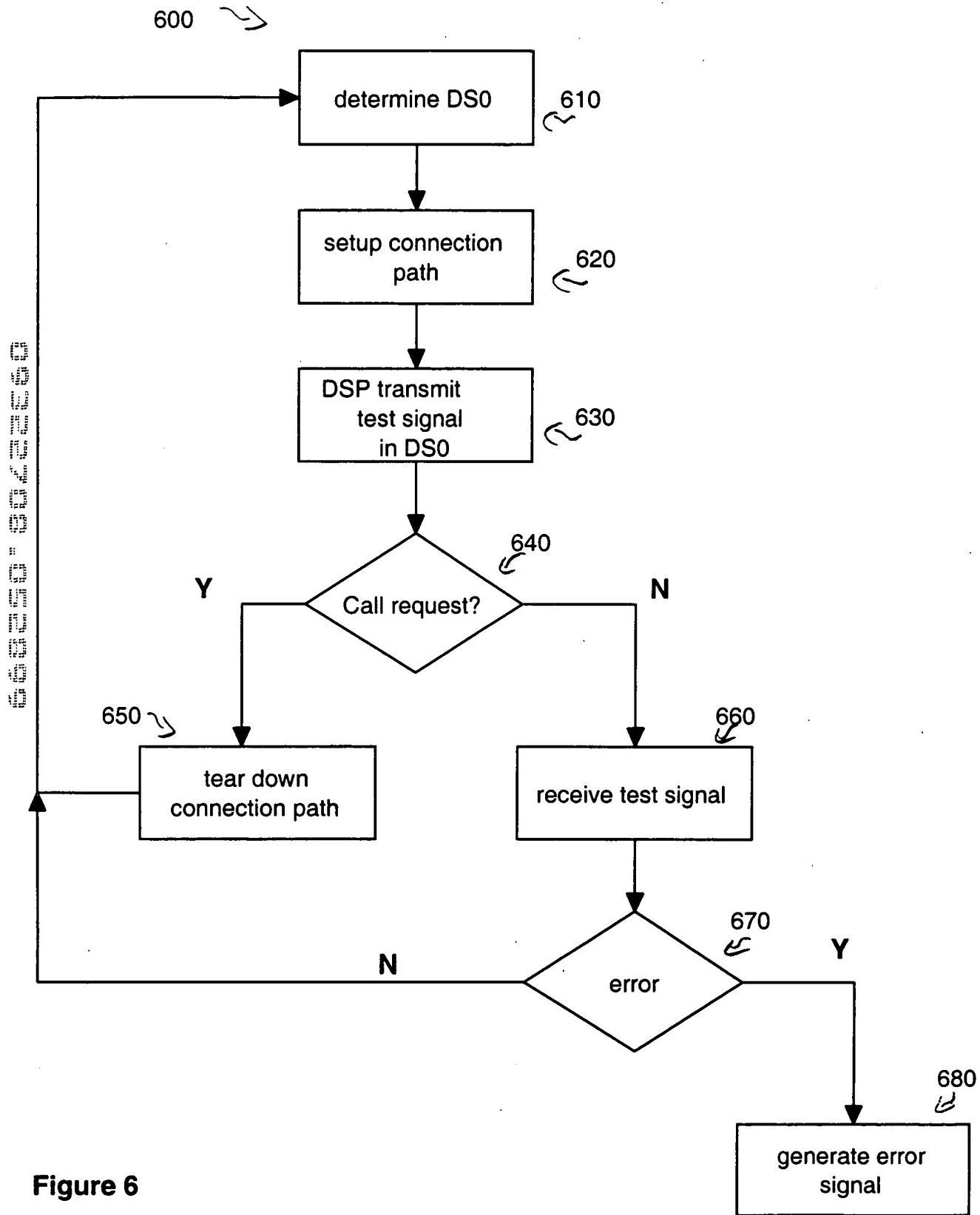


Figure 6